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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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 PATENT & TRADEMARK OFFICE

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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**COMPLETE IF KNOWN**

Application Number	10/033,340
Confirmation Number	7237
Filing Date	December 26, 2001
First Named Inventor	Suan Jeung Boon
Group Art Unit	2811
Examiner Name	Not yet assigned
Attorney Docket No.	108298631US

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIALS*	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		NUMBER	Kind Code (if known)			
YH	AA	U.S. Application No. 09/981,948		Boon et al.	Filed 10/16/01	
YH	AB	5,841,193		Eichelberger	11/24/98	
	AC					
	AD					
	AE					
	AF					
	AG					

**FOREIGN PATENT DOCUMENTS**

*EXAMINER INITIALS*	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T
		Office	Number	Kind Code (if known)				
	AH							
	AI							
	AJ							
	AK							
	AL							

**OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS**

*EXAMINER INITIALS*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume/issue number(s), publisher, city and/or country where published.	T
YH	AM	Zwenger and Tessier, "Technologies and Trends in Wafer Level Packaging," Enabling a Microelectronic World, Amkor Technology, Inc., 29 pages (2001).	
YH	AN	EPIC CSP™ Chip Scale Package Technology, "Low Cost Area Array Package Fully Integrates Printed Circuit, Chip Interconnect and Assembly," EPIC Technologies, Inc., 30 pages.	
	AO		
	AP		

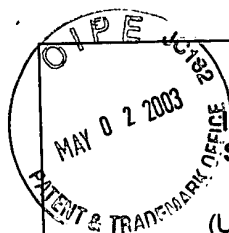
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**COMPLETE IF KNOWN**

Application Number	10/033,340
Confirmation Number	7237
Filing Date	December 26, 2001
First Named Inventor	Boon Suan Jeung
Group Art Unit	2818
Examiner Name	Mai Huong C. Tran
Attorney Docket No.	108298631US

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**U.S. PATENT DOCUMENTS**

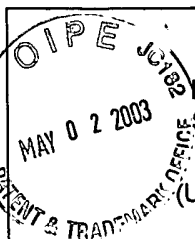
Examiner Initials*	Cite No.	U.S. Patent or Application		Name of Patentee or Inventor of Cited Document	Date of Publication or Filing Date of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		NUMBER	Kind Code (if known)			
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MH		5,252,857		Kane et al.	10/12/1993	
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Filing Date	December 26, 2001
First Named Inventor	Boon Suan Jeung
Group Art Unit	2818
Examiner Name	Mai Huong C. Tran
Attorney Docket No.	108298631US

 Sheet **2** of **2**
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MAH		LAU, JOHN H. and SHI-WEI RICKY LEE, <i>Chip Scale Package (CSP). Design, Materials, Processes, Reliability, and Applications</i> , pp. 355, 487-488, ISBN 0-07-038304-9, February 1999, McGraw-Hill, New York.	

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MA	AA	6,025,640		Yagi et al.	02/15/00	
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